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** CONTINUING DATA *****

This application is a CON of 09/965,220 09/28/2001 PAT 6,723,631

** FOREIGN APPLICATIONS *****

JAPAN 2000-300853 09/29/2000

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 04/20/2004

Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY JAPAN	SHEETS DRAWING 79	TOTAL CLAIMS 16	INDEPENDENT CLAIMS 1
35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance	EXAMINER'S SIGNATURE 	INITIALS 		
Verified and Acknowledged				

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TITLE

Fabrication method of semiconductor integrated circuit device

☐ All Fees☐ 1.16 Fees (Filing)